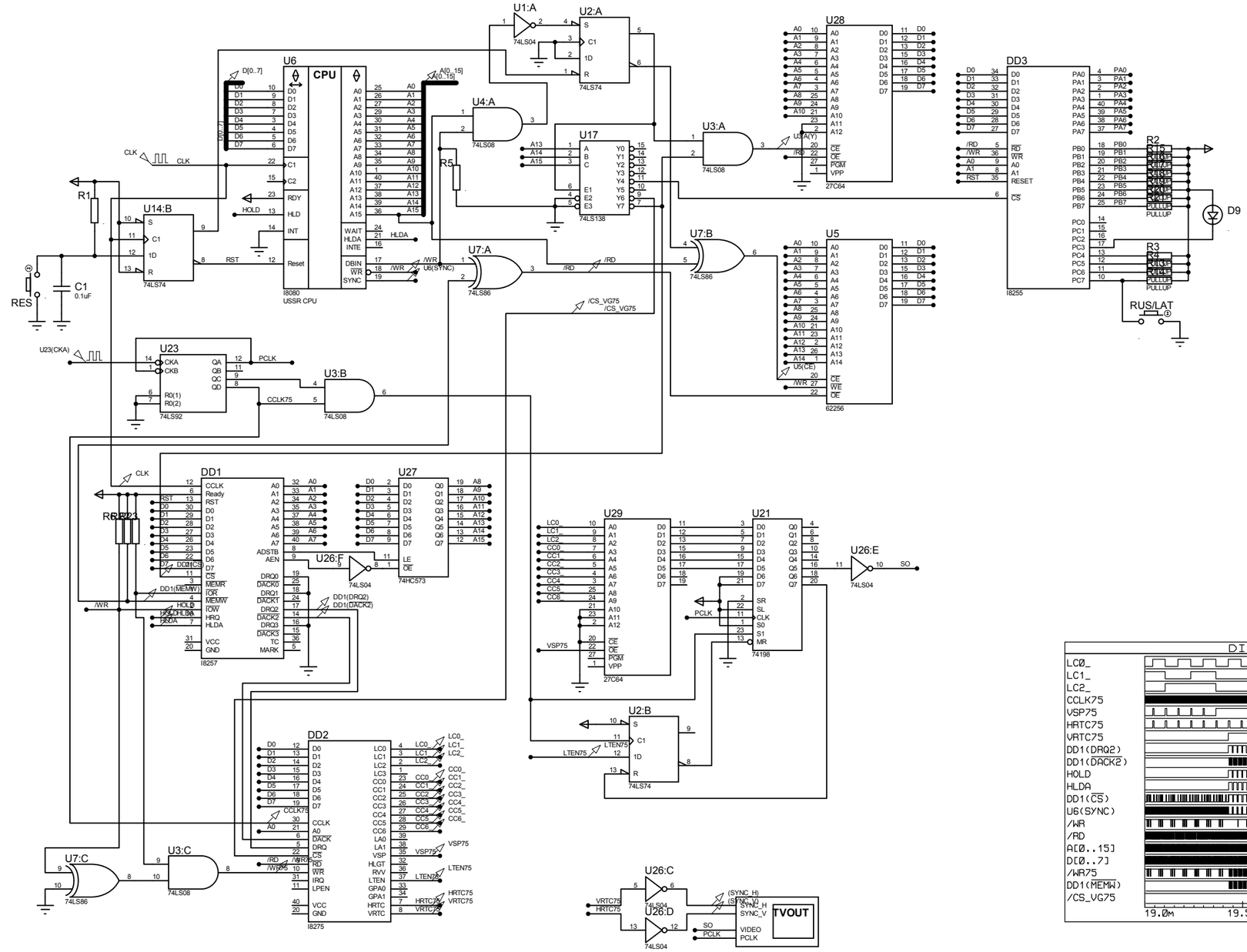
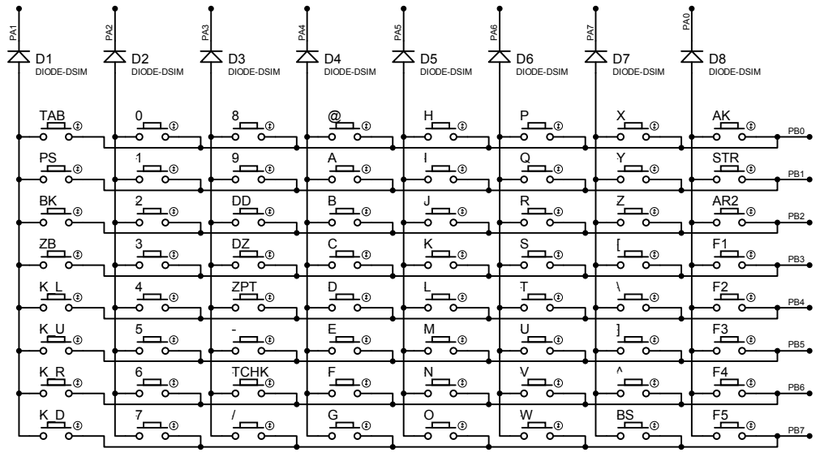
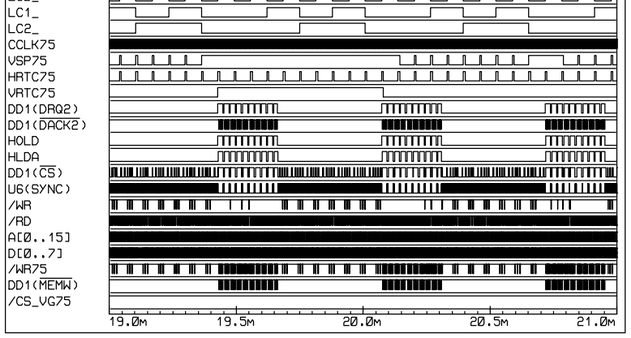

U1:A
74LS04
U2:A
74LS74
U6
CPU
8080
USSR CPU
U4:A
74LS08
U17
74LS138
U3:A
74LS08
U28
27C64
DD3
8255
R2
R3
PULLUP
D9
RUS/LAT
R1
U14:B
74LS74
C1
0.1uF
RES
HOLD
HLDA
/WR
/RD
U6(SYNC)
U7:A
74LS86
U7:B
74LS86
U5
62256
/CS_VG75
U23
74LS92
U23(CKA)
PCLK
U3:B
74LS08
CCLK75
DD1
8257
U27
74HC573
U26:F
74LS04
DD1(DRQ2)
DD1(DACK2)
U29
27C64
U21
74198
U26:E
74LS04
SO
VSP75
PCLK
U2:B
74LS74
LTEN75
DD2
8275
U7:C
74LS86
U3:C
74LS08
U26:C
74LS04
U26:D
74LS04
(SYNC_H)
(SYNC_V)
SYNC_H
SYNC_V
VIDEO
PCLK
TVOUT


PA1
PA2
PA3
PA4
PA5
PA6
PA7
PA0
D1 DIODE-DSIM
D2 DIODE-DSIM
D3 DIODE-DSIM
D4 DIODE-DSIM
D5 DIODE-DSIM
D6 DIODE-DSIM
D7 DIODE-DSIM
D8 DIODE-DSIM
TAB 0 8 @ H P X AK PB0
PS 1 9 A I Q Y STR PB1
BK 2 DD B J R Z AR2 PB2
ZB 3 DZ C K S [F1 PB3
K_L 4 ZPT D L T \ F2 PB4
K_U 5 - E M U] F3 PB5
K_R 6 TCHK F N V ^ F4 PB6
K_D 7 / G O W BS F5 PB7


DIGITAL ANALYSIS
LC0_
LC1_
LC2_
CCLK75
VSP75
HRTC75
VRTC75
DD1(DRQ2)
DD1(DACK2)
HOLD
HLDA
DD1(CS)
U6(SYNC)
/WR
/RD
A[0..15]
D[0..7]
/WR75
DD1(MEMW)
/CS_VG75
19.0M
19.5M
20.0M
20.5M
21.0M