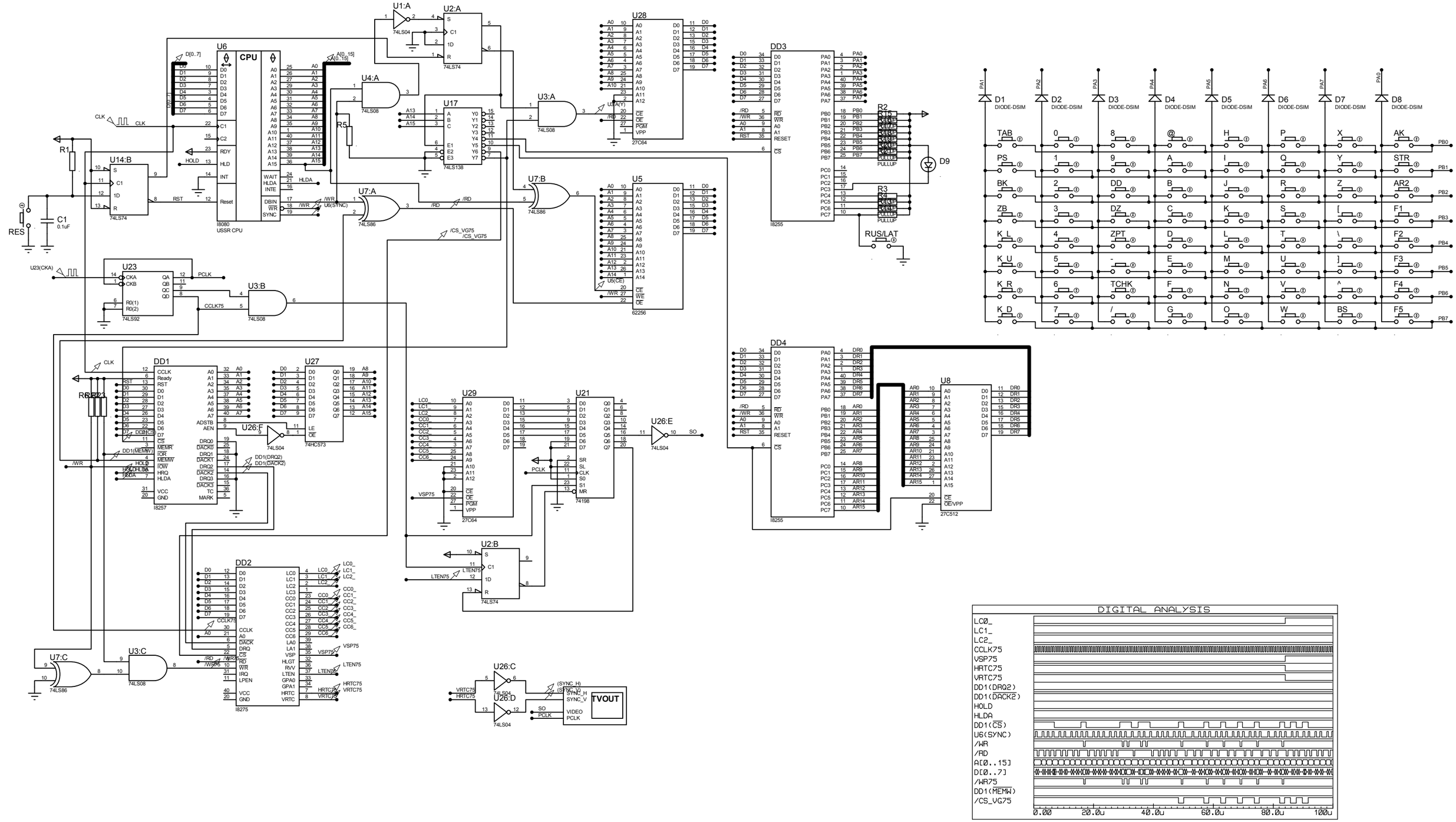
U1:A
74LS04
U2:A
74LS74
U4:A
74LS08
U6
CPU
8080
USSR CPU
U14:B
74LS74
U17
74LS138
U3:A
74LS08
U28
27C64
DD3
8255
U5
62256
U7:A
74LS86
U7:B
74LS86
U23
74LS92
U3:B
74LS08
DD1
8257
U27
74HC573
U26:F
74LS04
U29
27C64
U21
74198
U26:E
74LS04
DD4
8255
U8
27C512
U7:C
74LS86
U3:C
74LS08
DD2
8275
U2:B
74LS74
U26:C
U26:D
74LS04
TVOUT
R1
C1
0.1uF
RES
R5
R2
R3
D9
RUS/LAT
PULLUP
CLK
HOLD
RST
/WR
/RD
/CS_VG75
PCLK
CCLK75
VSP75
LTEN75
HRTC75
VRTC75
SO
VIDEO
SYNC_H
SYNC_V
D1 DIODE-DSIM
D2 DIODE-DSIM
D3 DIODE-DSIM
D4 DIODE-DSIM
D5 DIODE-DSIM
D6 DIODE-DSIM
D7 DIODE-DSIM
D8 DIODE-DSIM
TAB
PS
BK
ZB
K_L
K_U
K_R
K_D
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DD
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TCHK
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STR
AR2
F1
F2
F3
F4
F5
BS
PB0
PB1
PB2
PB3
PB4
PB5
PB6
PB7

DIGITAL ANALYSIS
LC0_
LC1_
LC2_
CCLK75
VSP75
HRTC75
VRTC75
DD1(DRQ2)
DD1(DACK2)
HOLD
HLDA
DD1(CS)
U6(SYNC)
/WR
/RD
A[0..15]
D[0..7]
/WR75
DD1(MEMW)
/CS_VG75
0.00
20.0u
40.0u
60.0u
80.0u
100u