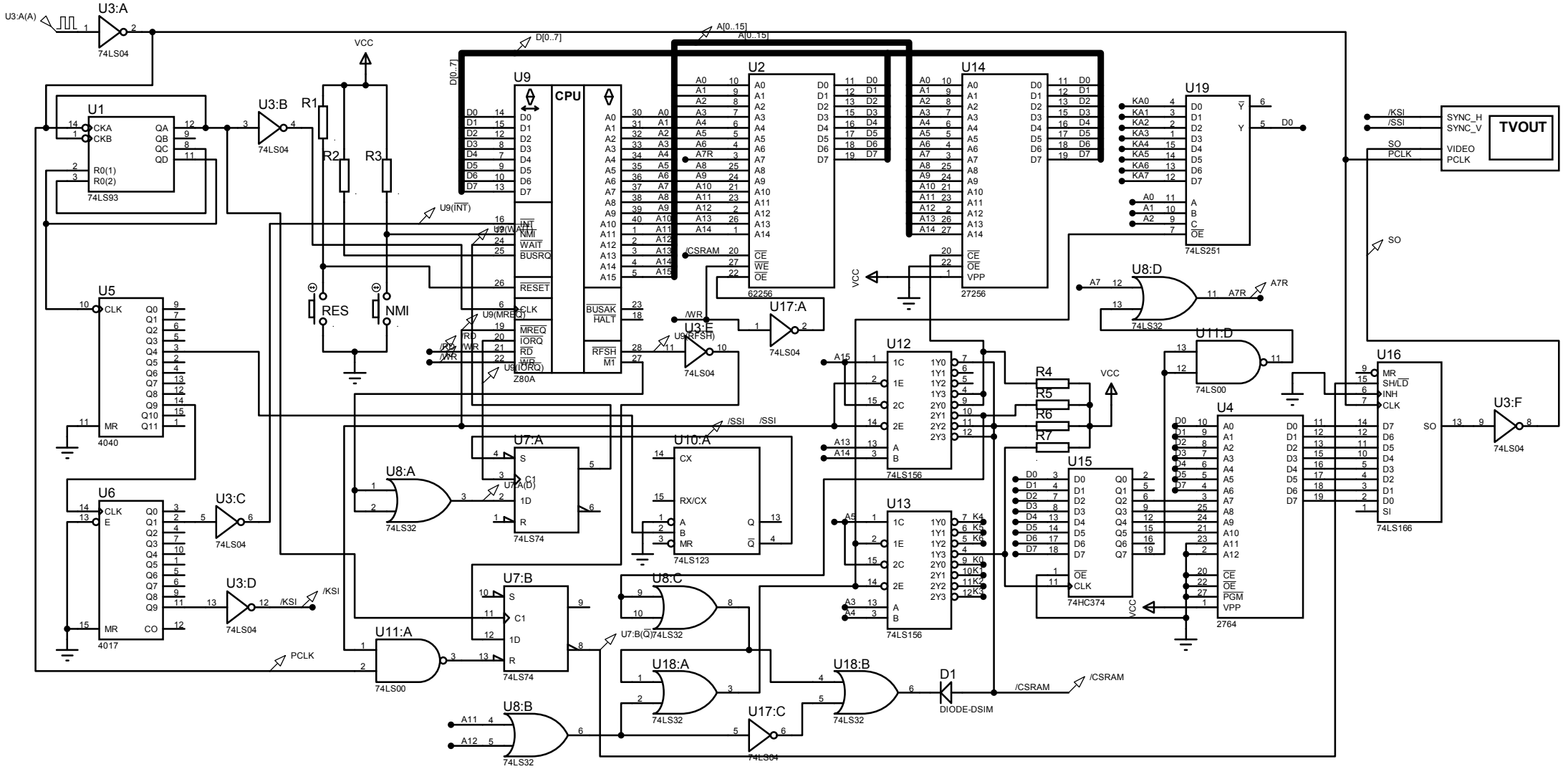
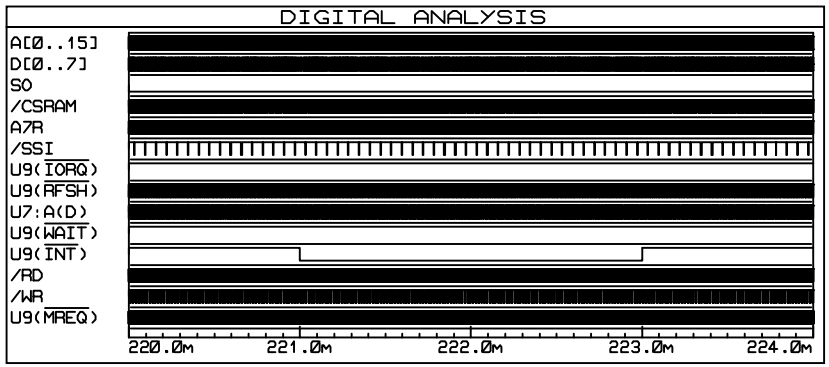

U3:A(A)
U3:A
74LS04
U1
CKA
CKB
QA
QB
QC
QD
R0(1)
R0(2)
74LS93
U3:B
74LS04
R1
R2
R3
VCC
D[0..7]
A[0..15]
U9
CPU
U2
62256
U14
27256
U19
74LS251
KA0
KA1
KA2
KA3
KA4
KA5
KA6
KA7
/KSI
/SSI
SO
PCLK
SYNC_H
SYNC_V
VIDEO
PCLK
TVOUT
U9(INT)
U9(WAIT)
U9(MREQ)
U9(IORQ)
U9(RFSH)
Z80A
INT
NMI
WAIT
BUSRQ
RESET
CLK
MREQ
IORQ
RD
WR
BUSAK
HALT
M1
RES
NMI
/RD
/WR
/CSRAM
CE
WE
OE
VPP
U17:A
74LS04
U3:E
74LS04
U8:D
74LS32
A7R
U11:D
74LS00
U16
MR
SH/LD
INH
CLK
SI
74LS166
U3:F
74LS04
U5
4040
MR
U6
4017
CLK
E
MR
CO
U3:C
74LS04
U3:D
74LS04
/KSI
U8:A
74LS32
U7:A
74LS74
1D
R
U10:A
74LS123
CX
RX/CX
U12
74LS156
R4
R5
R6
R7
U13
74LS156
U15
74HC374
U4
2764
PGM
VPP
U7:B
74LS74
U8:C
74LS32
U7:B(Q)
U11:A
74LS00
PCLK
U18:A
74LS32
U18:B
74LS32
U8:B
74LS32
A11
A12
U17:C
74LS04
D1
DIODE-DSIM
/CSRAM
KN1
K1
KA2
R8
VCC


DIGITAL ANALYSIS
A[0..15]
D[0..7]
SO
/CSRAM
A7R
/SSI
U9(IORQ)
U9(RFSH)
U7:A(D)
U9(WAIT)
U9(INT)
/RD
/WR
U9(MREQ)
220.0M
221.0M
222.0M
223.0M
224.0M