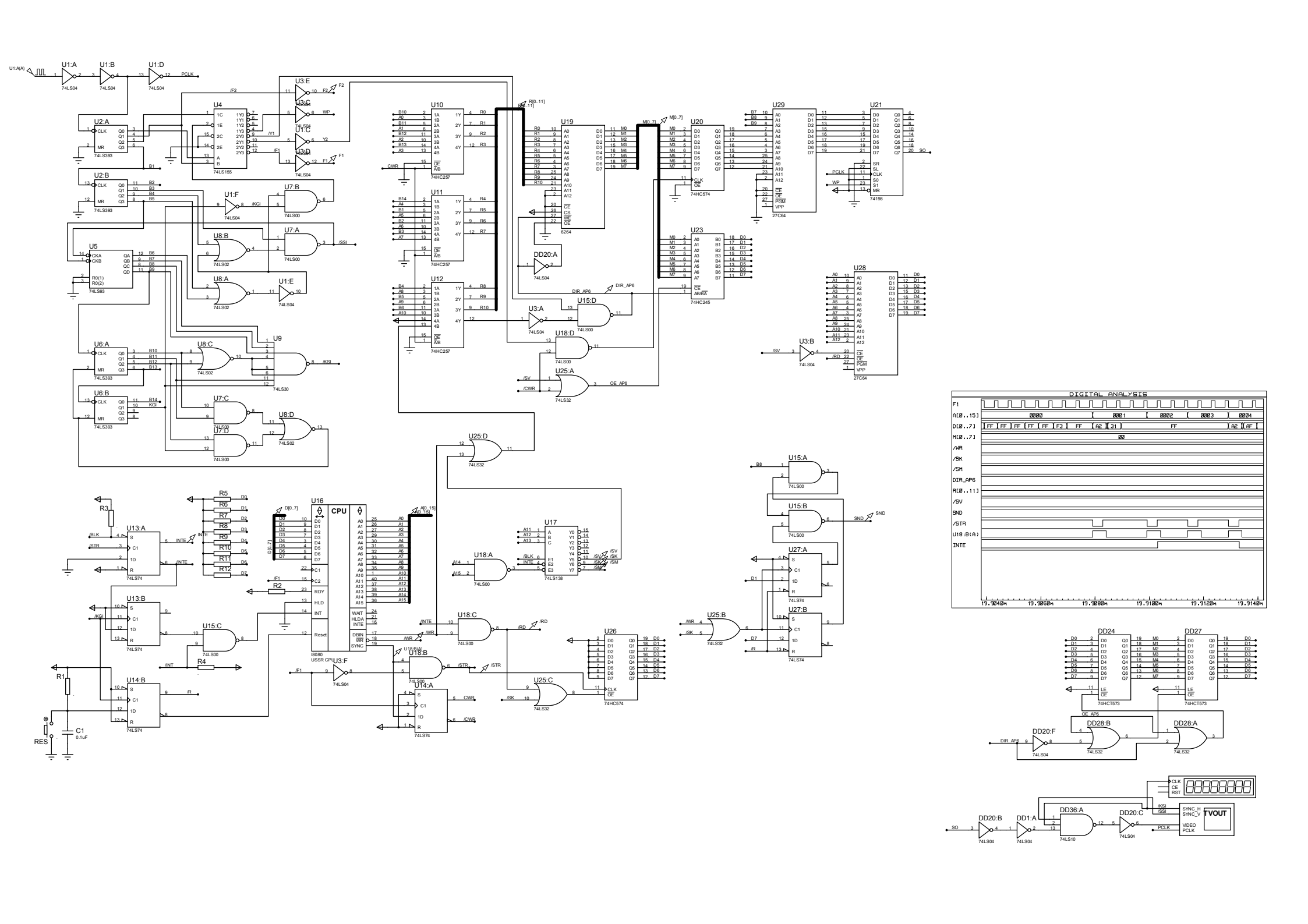
U1:A(A)
U1:A
U1:B
U1:D
74LS04
PCLK
/F2
U3:E
F2
U3:C
WP
Y2
U1:C
/Y1
/F1
U3:D
F1
U4
74LS155
U2:A
U2:B
74LS393
B1
U1:F
/KGI
U7:B
U7:A
/SSI
74LS00
U8:B
U8:A
74LS02
U1:E
U5
74LS93
R0(1)
R0(2)
U6:A
U6:B
U8:C
U9
/KSI
74LS30
U7:C
U8:D
U7:D
U10
74HC257
R0..11]
R[0..11]
U11
U12
CWR
U19
M[0..7]
74HC574
U20
6264
U29
27C64
U21
74198
PCLK
WP
SR
SL
S0
S1
MR
SO
U23
74HC245
DIR_AP6
U15:D
U18:D
U3:A
DD20:A
U25:A
/SV
/CWR
OE_AP6
U28
U3:B
/RD
U25:D
74LS32
U15:A
U15:B
SND
U27:A
U27:B
74LS74
U25:B
/WR
/SK
U13:A
U13:B
U14:B
U15:C
U14:A
/BLK
/STR
/INTE
/KGI
/INT
/F1
R1
R2
R3
R4
R5
R6
R7
R8
R9
R10
R11
R12
C1
0.1uF
RES
U16
CPU
D[0..7]
A[0..15]
WAIT
HLDA
INTE
DBIN
WR
SYNC
RDY
HLD
INT
Reset
I8080
USSR CPU
U17
74LS138
U18:A
U18:C
U18:B
U18:B(A)
U3:F
U14:A
CWR
/CWR
U26
74HC574
U25:C
/ISK
DD24
DD27
74HCT573
LE
OE
OE_AP6
DD20:F
DD28:B
DD28:A
DIR_AP6
DIGITAL ANALYSIS
F1
A[0..15]
D[0..7]
M[0..7]
/WR
/SK
/SM
DIR_AP6
A[0..11]
/SV
SND
/STR
U18:B(A)
INTE
0000
0001
0002
0003
0004
FF
A2
31
AF
00
19.9040m
19.9060m
19.9080m
19.9100m
19.9120m
19.9140m
DD20:B
DD1:A
DD36:A
DD20:C
74LS10
CLK
CE
RST
/KSI
/SSI
SYNC_U
SYNC_V
VIDEO
PCLK
FVOUT